

Fig.1

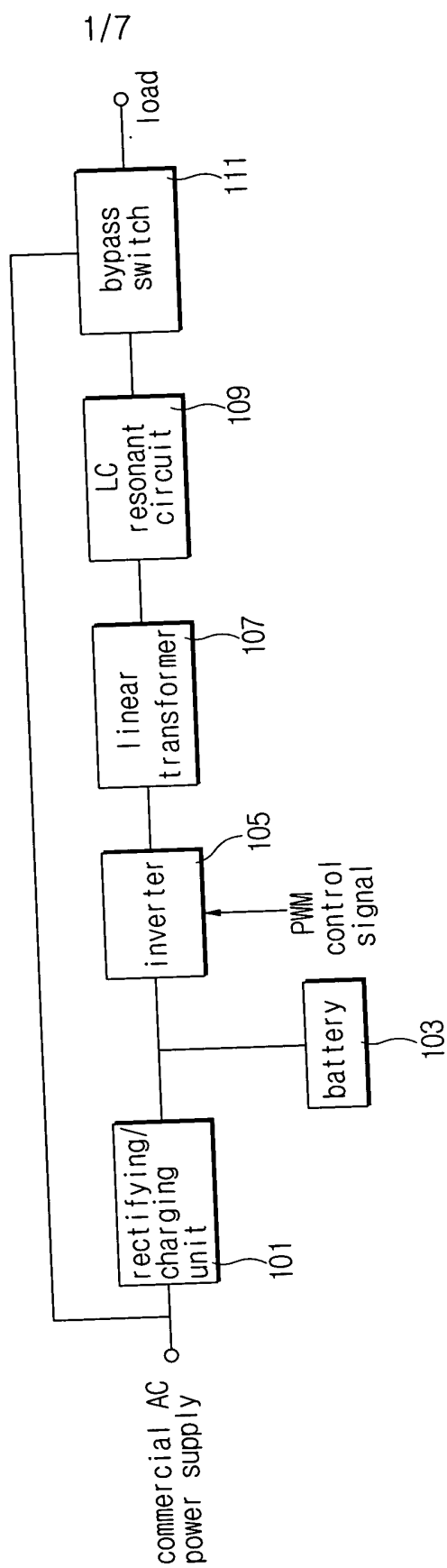


Fig.2

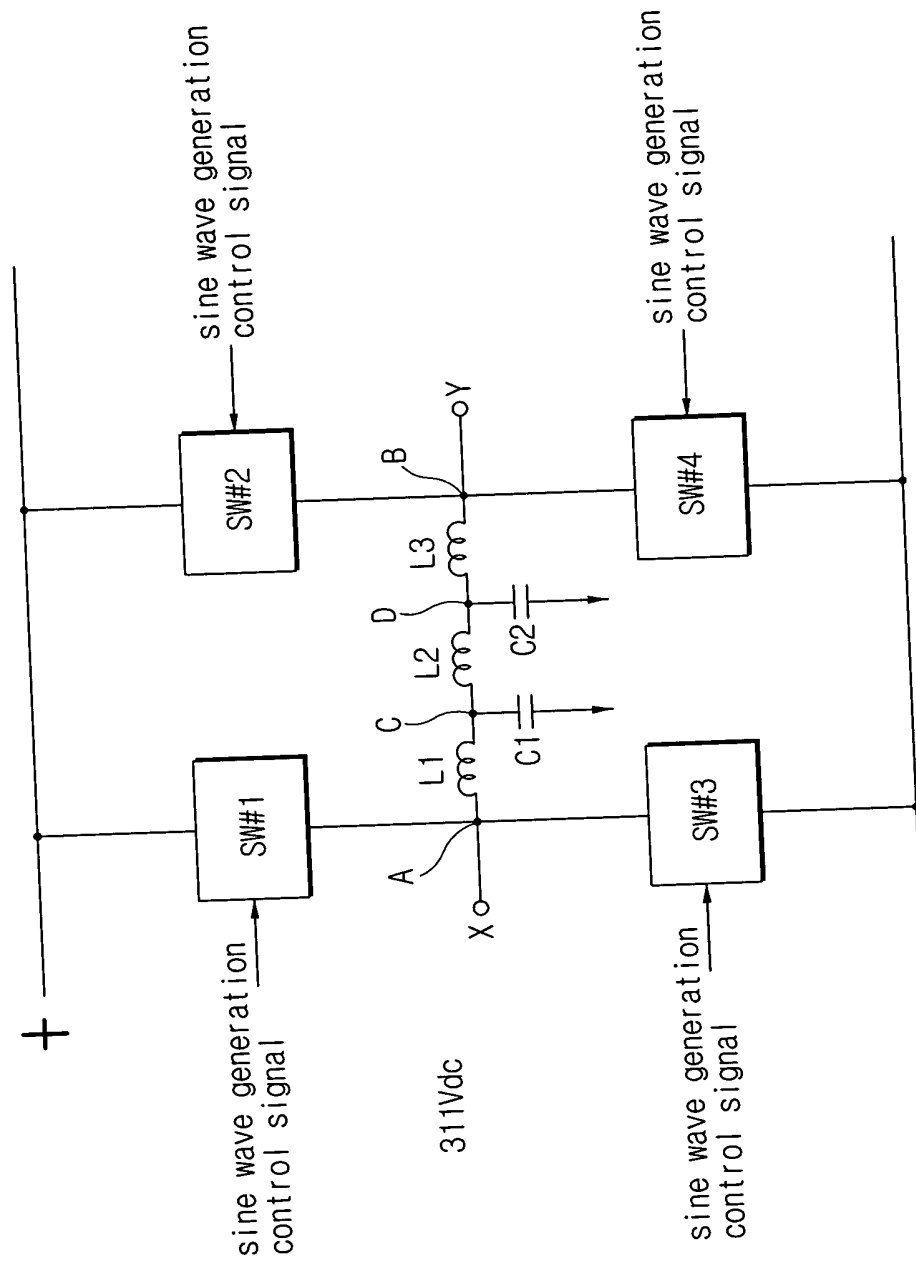


Fig.3

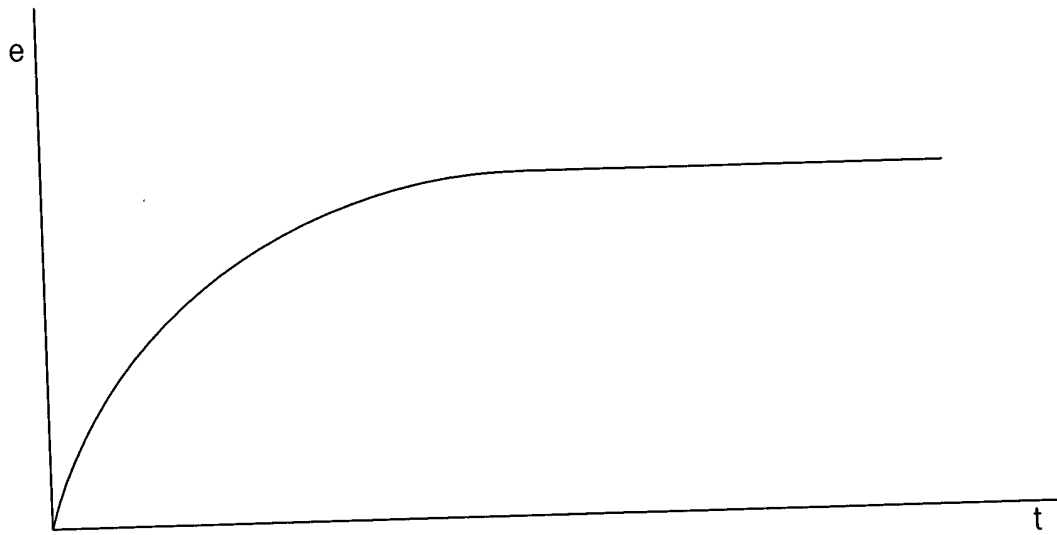


Fig.4

t1	51	52	53	54	---	98	99	99	98	---	52	51	50	49	48	---
t2	49	48	47	46	---	2	1	1	2	---	48	49	50	51	52	---
output voltage	2	4	6	8	---	96	98	98	96	---	4	2	0	-2	-4	---

t1 → 1, 2, 3, ---- 97, 98, 99, 99, 98, 97 ---- 3, 2, 1

(t1 indicates turn-on time of SW1 and SW4) — ①

t2 → 99, 98, 97, ---- 3, 2, 1, 1, 2, 3, ---- 97, 98, 99

(t2 indicates turn-on time of SW2 and SW3) — ②

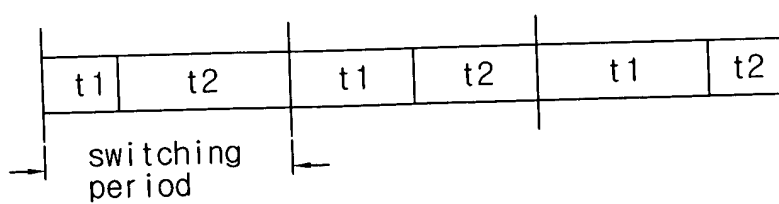
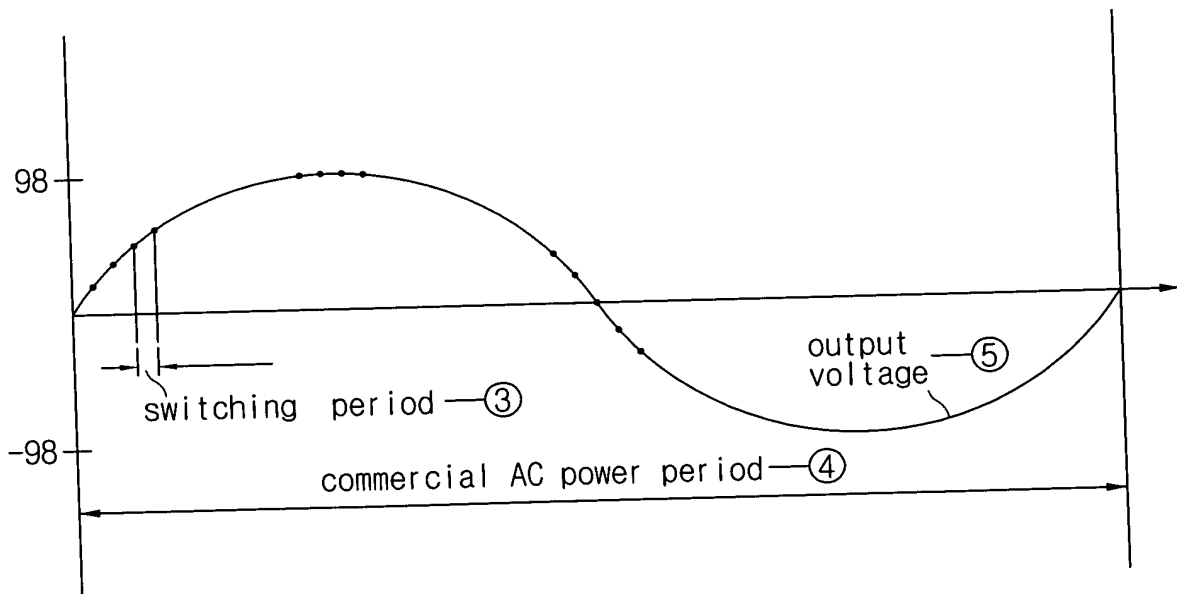


Fig.5

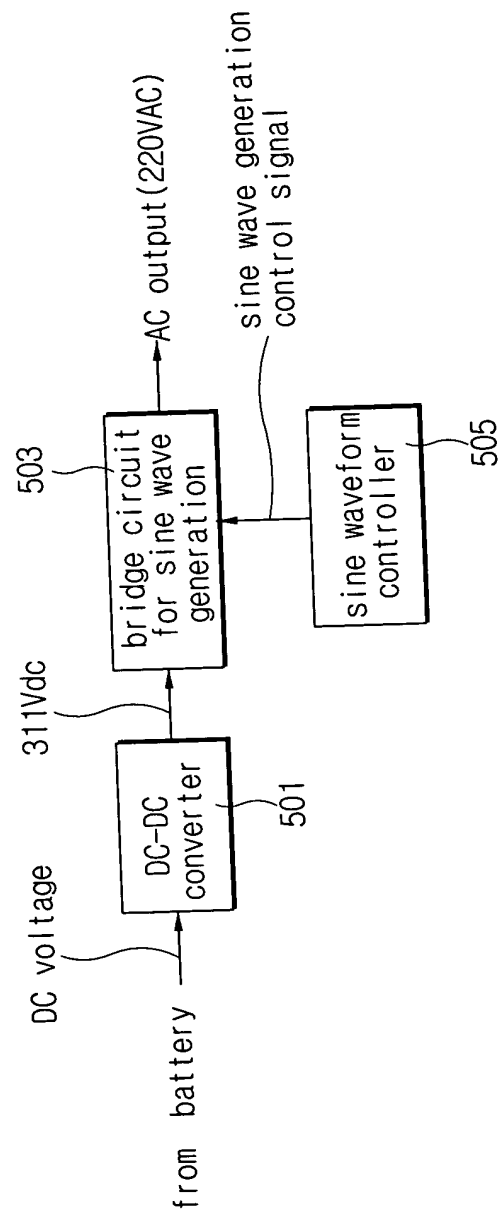


Fig.6

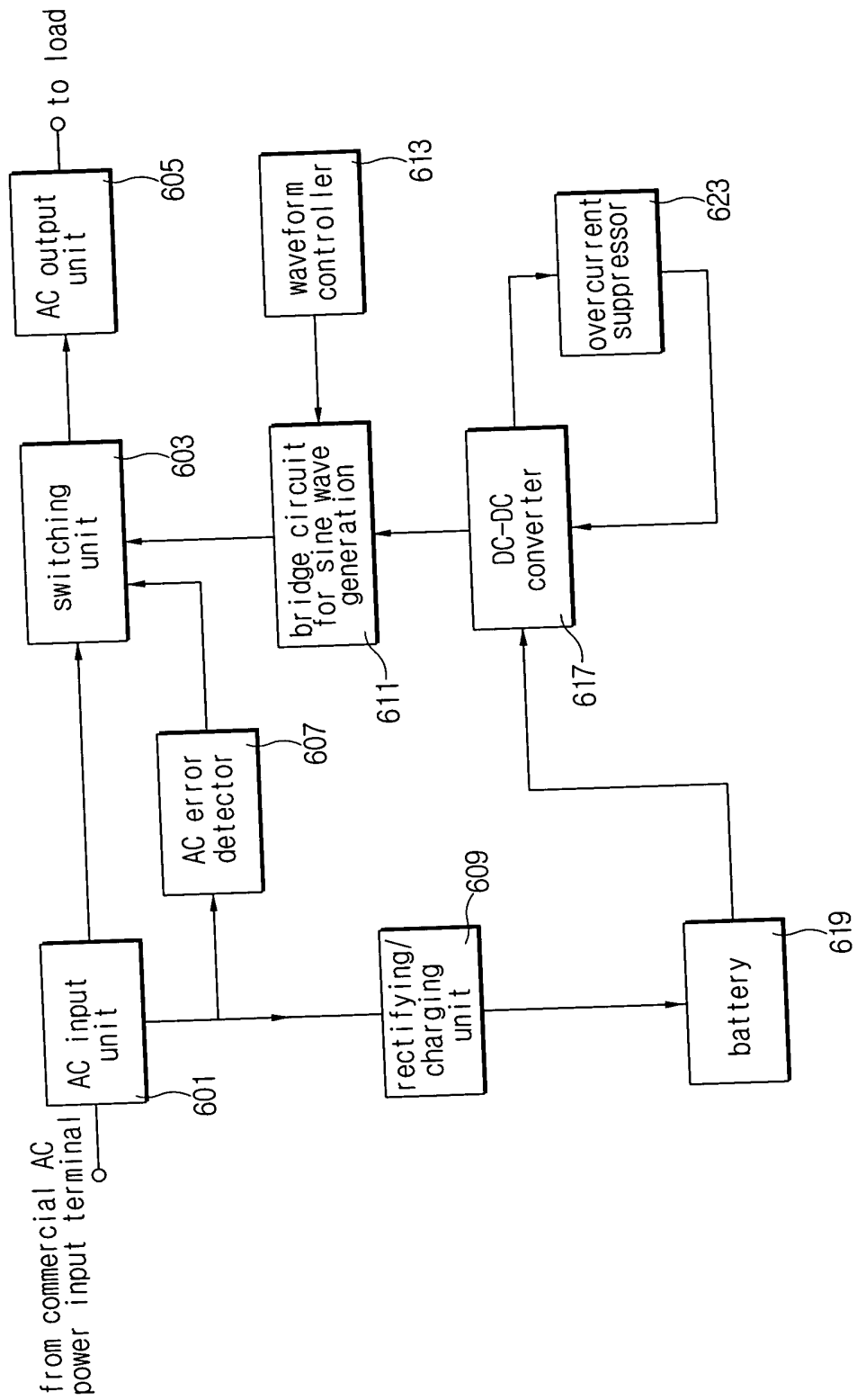


Fig.7

